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PATENT 790001-2004

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THANKS E UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants

Hiroyuki YANO et al.

Serial No.

09/870,085

Filed

May 30, 2001

For

MANUFACTURING METHOD OF SEMICONDUCTOR

DEVICE

Examiner

Thanhha Pham

Group Art Unit

2813

745 Fifth Avenue New York, New York 10151

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I hereby certify that this paper or fee is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" Service under 37 CFR 1.10 on the date indicated above and is addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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COMMUNICATION

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

As noted in the Preliminary Amendment filed on August 25, 2003, attached is a Declaration under 37 C.F.R. §1.132 supporting the drawing correction submitted with the Amendment.

TECHNOLOGY CENTER 2800

Applicants submit that the application is now in condition for allowance, which action is earnestly solicited.

If any issues remain, or if the Examiner has any further suggestions, he/she is invited to call the undersigned at the telephone number provided below.

The Examiner is hereby authorized to charge any insufficient fees or credit any overpayment associated with the above-identified application to Deposit Account No. 50-0320.

The Examiner's consideration of this matter is gratefully acknowledged.

Respectfully submitted, FROMMER LAWRENCE & HAUG LLP

Grace L. Pan

Registration No. 39,440

Bruno Polito

Registration No. 38,580

Tel. (212) 588-0800

Fax (212) 588-0500